

EMC

JOINT WORKSHOP 2025

Call For Paper

November 6–7,
2025
@Ho Chi Minh

The EMC Joint Workshop 2025 will take place on November 6–7, 2025, in Ho Chi Minh City, Vietnam, co-hosted by QUATEST3 and the Technical Committee on Electromagnetic Compatibility (EMCJ) of IEICE. This international workshop provides a platform for researchers, engineers, and industry professionals to exchange knowledge and discuss the latest electromagnetic compatibility (EMC) advancements. We invite experts, researchers, and students to join discussions on cutting-edge EMC topics, fostering collaboration and innovative solutions for modern EMC challenges.

Topics (ALL EMI/EMC Researches can be accepted)

- EMC Measurement and Instrumentation
- Electromagnetic Environment
- Transient EMC
- Power System EMC
- System Level EMC and Protection
- EMC in Nanotechnology and Advanced Materials
- Signal Integrity and Power Integrity
- Computational Electromagnetics
- Biomedical Electromagnetics
- EMC for Wireless Power Transfer
- EMC Design
- Others

Important Dates

- Abstract Submission Deadline: **August 18, 2025**
- Final Manuscript Submission: **October 16, 2025**
- Registration Period: **Open until August 18, 2025**

Presentation Format

- Oral Presentation: 15-minute talk + 5-minute Q&A
- Poster Session: Interactive discussion session

Registration and Fees

- Details regarding registration fees will be announced soon

Venue

- QUATEST3 Cat Lai's Office / Testing Lab Complex
Slot C5, K1 Street, Cat Lai IDZ, Thu Duc City, Ho Chi Minh City, Vietnam

Organizer

- Chair : Yoshitaka Toyota
- Co-chair : Vu Thanh Cong
- TPC Chair : Kaoru Gotoh

Contact

- Yoshitaka Toyota : toyota@okayama-u.ac.jp
- Vu Thanh Cong : xt@quatest3.com.vn
- Kaoru Gotoh : k_gotoh@nict.go.jp